(19) World Intellectual Property Organization

International Bureau





(43) International Publication Date 21 April 2005 (21.04.2005)

PCT

(10) International Publication Number WO 2005/036582 A2

(51) International Patent Classification7:

H01J 9/00

(21) International Application Number:

PCT/EP2004/052509

- (22) International Filing Date: 12 October 2004 (12.10.2004)
- (25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data: 0323942.3

13 October 2003 (13.10.2003)

- (71) Applicant (for all designated States except US): THE FIELD EMISSION DISPLAY COMPANY LIMITED [GB/GB]; 6 Elm Grove, Hartlepool TS26 8LZ (GB).
- (72) Inventor; and
- (75) Inventor/Applicant (for US only): TUCK, Richard, Allan [GB/GB]; 34 Park Lane, Slough, Berkshire SL3 7PF (GB).
- (74) Agent: STANLEY, David; Stanleys, Kings Court, 12 King Street, Leeds Yorkshire LS1 2HL (GB).

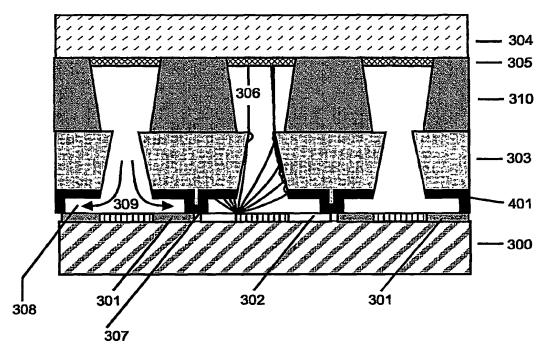
- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

without international search report and to be republished upon receipt of that report

[Continued on next page]

(54) Title: FIELD EMITTERS AND DEVICES



(57) Abstract: A Hop-FED structure has emitter areas (301, 302) on a substrate (300), and a hop-plate (303) disposed over the substrate (300) and emitter areas (301, 302) with a surface of the hop-plate (303) opposing the substrate (300) and emitter areas (301, 302). An electrically conductive layer (401) is formed on the surface of the hopplate (303), to dissipate charge build-up.

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